Sub C1

CLAIM SET AS AMENDED

1. (PREVIOUSLY PRESENTED) A quad type liquid crystal display device, comprising:

a liquid crystal panel having gate and data lines which define sub-pixel regions;

gate driving integrated circuits for driving the gate lines; and

a plurality of data drive integrated circuits arranged on one side of the liquid crystal panel, each of the data drive integrated circuits having "m" (m is a natural number) number of channels,

B1 cont wherein (3n-1)th (n is a natural number) channels for each data drive integrated circuit are floating.

2. (ORIGINAL) The device of claim 1, wherein each of two by two sub pixels corresponds to red, a first green, a second green, and blue color filters, respectively.

3. (ORIGINAL) The device of claim 1, wherein m is 384.

4. (ORIGINAL) The device of claim 1, wherein the number of data integrated circuits is four.

5. (PREVIOUSLY PRESENTED) A liquid crystal display panel;

a plurality of drive integrated circuits for driving the panel, each of said plurality of drive integrated circuits having "m" (m is a natural number) number of channels and "n" (n is a natural number) number of floating channels;

a plurality of films for connecting the drive integrated circuits, each film having (m-n) number of lines, wherein n<m.

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6. (PREVIOUSLY PRESENTED) The liquid crystal display panel of claim 5, wherein (3n-1)th channels are floating.

7. (PREVIOUSLY PRESENTED) The liquid crystal display panel of claim 6, wherein m is 384.

8. (CURRENTLY AMENDED) The ~~liquid crystal display panel~~ device of claim 1, wherein the data drive integrated circuits are located on only one side of the liquid crystal panel.

9. (NEW) The liquid crystal display panel of claim 5, wherein each of two by two sub pixels corresponds to red, a first green, a second green, and blue color filters, respectively.

10. (NEW) The liquid crystal display panel of claim 5, wherein the number of drive integrated circuits is four.

11. (NEW) The liquid crystal display panel of claim 5, wherein the drive integrated circuits are located on only one side of the liquid crystal panel.

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12. (NEW) The device of claim 2, wherein a first group of four sub-pixels for a first pixel have one of positive and negative polarity, and a next group of four sub-pixels for a next pixel have the other of positive and negative polarity, and remaining groups of four sub-pixels for remaining pixels alternate between positive and negative polarity.

13. (NEW) The liquid crystal display panel of claim 9, wherein a first group of four sub-pixels for a first pixel have one of positive and negative polarity, and a next group of four sub-pixels for a next pixel have the other of positive and negative polarity, and remaining groups of four sub-pixels for remaining pixels alternate between positive and negative polarity.

14. (NEW) The device of claim 1, wherein there are at least three of said plurality of data drive integrated circuits.

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15. (NEW) The liquid crystal display panel of claim 5, wherein there are at least three of said plurality of drive integrated circuits.